Shri Vaishnav Vidyapeeth Vishwavidyalaya
Master of Technology (Computer Science and Engineering)
Choice Based Credit System (CBCS)
SEMESTER - II

S. NO.	COURSE CODE	COURSE NAME	Examination Scheme					Teaching Scheme/Week			CREDITS	TOTAL MARKS
			THEORY			PRACTICAL						
			End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	Th	T	P		
1	MTCS201	Advance Computer Architecture & Organization	60	20	20	0	0	3	1	0	4	100
2	MTCS202	Advance Database Management System	60	20	20	30	20	3	1	2	5	150
3	MTCS203	Simulation and Modeling	60	20	20	30	20	3	1	2	5	150
4	MTCS210	Elective III	60	20	20	0	50	3	1	2	5	150
5	MTCS220	Elective IV	60	20	20	30	20	3	1	2	5	150
6	MTCS206	Network Analysis Lab	0	0	0	30	20	0	0	2	1	50
7	MTCS207	Software Construction Lab	0	0	0	0	50	0	0	2	1	50
8	MTCS208	Comprehensive viva	0	0	0	50	0	0	0	8	4	50
			300	100	100	170	180	15	5	20	30	850

Elective-III				Elective-IV	
MTCS211	Expert System			MTCS221	Network & Web Security
MTCS212	Grid Computing			MTCS222	Mobile Communication
MTCS213	Human Computer Interaction			MTCS223	Wireless Sensor Adhoc Network
MTCS214	Embedded System			MTCS224	Distributed Computing

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chairperson
Board of Studies
(Computer Science & Engineering,
Information Technology & Computer Applications)
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore

Deputy Registrar
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore